



# IP solutions catalog 2025

# SPIDER IPs: State-of-the art solution for Power Management

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As a three-decade leader in Power Management IP, we've tailored solutions to fit your exact design needs, saving you time and resources while accelerating your time-to-market and boosting design productivity.

			Power Man	agement IP plat	form					
		V <sub>IN</sub> (V)	Vout (V)	Ιουτ	lq (μA)	180nm	55nm	40nm	22nm	12nm
	SIMO DC/DC Single Input Multiple Outputs	[1.8 - 5.5]	[0.5 -3.5]	Up to 400 mA	0.64 to 1.5 µA				~	0
		[1.62 - 5.5]	[0.5 - 3.3]		0.35 to 0.37 µA			~	~	0
	DC/DC - Low Quiescent (LQ)	[1.62 - 3.63]	[0.5 -2.5]	100 mA – 1 A	0.29 to 0.37 µA			~	~	
J	DC/DC - Mainstream	[1.9 - 5.5]	[0.5 - 3.3]		80 to 100 µA				~	
DC/DC Buck		[2.7 - 5.5]	[0.55 - 3.3]	100 mA – 1 A	130 to 187 µA			~		
C/D		[1.62 - 3.63]	[0.6 - 3.3]		75 to 100 μA	~	~	~	~	
Õ	DC/DC - Legacy	[1.9 - 4.4]	[0.6 - 3.3]	100 mA – 1 A	70 to 125 µA	~	~			
	<b>Combo DC/DC</b> Mainstream DC/DC + LQ LDO	[1.9 - 5.5]	[0.5 - 3.3]		(sleep) = 0.25 to 0.6 μA (active) = 100 to 130 μA				~	
		[2.7 - 5.5]	[0.55 - 3.3]	100 mA – 1 A	(sleep) = 0.37 μA (active) = 155 μA			~		
		[1.62 - 3.63]	[0.55 - 3.3]	100 mA - 700 mA	(sleep) = 0.14 to 0.37 μA (active) = 75 to 100 μA	~	~	~	~	
LDD	<b>LDO • High Performances (HP)</b> Fast transient response High PSRR	[1.8 - 5.5]	[0.5 - 3.3]		25 to 65 µA			~	~	0
		[2.7 - 5.5]	[0.55 - 3.3]	100 mA - 500 mA	40 to 125 µA	~		~		
		[1.44 - 1.98]	[0.6 - 1.2]		40 µA				~	
		[1.62 - 3.63]	[0.6 - 2.5]	-	23 to200 µA		~	~	~	
	LDO - Low Quiescent (LQ)	[2.7 -5.5]	[0.55 - 3.3]	up to 1 mA	0.37 µA			~		
		[1.9 - 4.4]	[0.55 - 3.3]	up to 1 mA	0.075 to 0.17 µA	~	~			
		[1.62 - 3.63]	[0.5 - 3.3]	up to 2 mA	0.14 to 0.37 µA	~	$\checkmark$	~	~	0
	LDO - Capacitor Less (CL)	[0.72 -1.8]	[0.5 - 0.9]	upto50 mA	7.5 to 12 µA				~	0
	Combo LDO	[2.7 -5.5]	[0.5 - 2.5]		(sleep) = 0.37 μA (active) = 40 μA			~		
	HP LDO + LQ LDO	[1.62 - 3.63]	[0.55 - 2.5]		(sleep) = 0.14 to 0.16 μA (active) = 45 to 75 μA		~		~	
		[0.72 - 0.99]			lq = 97 to 165 nA - CL= 4 to 12.5 pF Accuracy = ±50 ppm				~	~
ORS	32 kHz XTAL	[0.81 - 1.21]			lq = 50 to 120 nA / / CL = 4 to 7 pF		$\checkmark$	~		
OSCILLATORS		[O. 72 - 0.88]	Freq = Iq = 400 nA // Accuracy (full V & T   32.768 kHz ranges): ±500 ppm@ 3a (10 sec n						0	
osci	32 kHz RC	[0.54 - 0.99]	lq = 55 to 70 nA Accuracy (after trimming) lq = 70 to 420 nA Accuracy (after trimming)						~	0
		[0.495 - 1.21]					$\checkmark$	~		
TORS		[1.62 - 3.63]	Monitored (V) Monitored (V) Monitored (V) Monitored (V) Monitored (V) Monitored (V) Monitored (V)				$\checkmark$	~	~	0
MONITORS	POR-BOR	[1.44 - 1.98]	= [0-AVD]		16 μA (continuous operation) < 150 nA (burst operation) < 100 nA (BOR disabled)				~	
	RTC (RTL IP)	Core voltage			Accuracy: -2.1 to 1.6 ppm Nominal freq: 32.768 kHz	~	~	~	~	0
2	Adaptative Body Bias (ABB)								~	
PMU		GUI-based power controller configurator (RTL & C-drivers)					~	~	~	0
	Configurable Power Controller	GUI-based UPF backbone generator						~	~	0



# BAT IPs: Amplifying Audio Excellence

For over 30 years, we are leading Audio premium IPs, catering to diverse applications including TWS, Smart speakers,Wearables, IoT, Automotive, and more. Our BAT IP family offers seamless configurability and assembly for high-fidelity, low-power audio devices, ensuring faster time-to-market with robust and advanced IPs.

Mixed signal Voice & Audio IP platform												
	Power supply	SNR	THD+N	Input noise	Power consumption	55nm	40nm	28nm	22nm	12nm		
Voice/audio ADC - Performance	1.8 V +/-10%	107 dB	-98dB	3.1 µVrms	Full perf. mode: 250 µA Low power mode: 125 µA				~	$\checkmark$		
Voice/audio ADC - Mainstream+	1.8 V +/-10%	104.5 dB	- 75 dB	2.6 µVrms	Full perf. mode: 550 μA Low power mode: 150 μA				~	$\checkmark$		
	1.8 Vto 3.3 V	109 dB	- 75 dB	2.6 µVrms	Full perf. mode: 550 μA Low power mode: 150 μA		0		0			
Voice/audio ADC	1.8 V +/-10%	101 dB	-80 dB	3.8 µVrms	Full perf. mode: 550 μA Low power mode: 170 μA				~	$\checkmark$		
– Mainstream	1.8 Vto 3.3 V	106 dB	-80 dB	3.8 µVrms	Full perf. mode: 550 µA Low power mode:		~		$\checkmark$			
1 Voice/audio ADC	2.7 Vto 3.63 V	90dB	-90 dB	7.7 μVrms	Full perf. mode: 1.7 mA	~	~					
- Legacy	1.8 V +/-10%	85dB	-80 dB	8 µVrms	Full perf. mode: 1.7 mA			~	~			
							0 = r	badmap /	Under dev	/elopme		

		Power supply	SNR	THD+N	Output noise	Power consumption					
	) DAC + class-D - performance	1.8 V +/-10%	115 dB	-95 dB	1.78 µVrms	915 µA without load 1.055 mA at 0.1 mW				0	~
Audio	Audio DAC + class-AB	1.8 V +/-10%	115 dB	-90 dB	1.9 µVrms	1,175 µA without load 3.4 mA at 0.1 mW				~	
amp.	- Mainstream	1.8 Vto 3.3 V	120 dB	-90 dB	1.9 µVrms	1,175 µA without load 3.4 mA at 0.1 mW				~	
Audio	DAC + class-AB - Legacy	2.97 V to 3.63 V	100 dB	-90 dB	6 µVrms	2,200 µA without load	~	~			
								0 = I	oadmap /	/ Under de	velopm

	Smart Audio IP platform											
		Input signal	Output Signal	Main Clock	Power Consumption	55nm	40nm	28nm	22nm	12nm		
IUV	<b>Combo DC/DC</b> Mainstream DC/DC + LQ LDO	Analog microphone	IRQ upon voice detection	32 kHz RC or 32 kHz crystal	13 µW @ 40 & 22nm	~	~		~			
		Digital microphone	IRQ upon voice detection	From 6 MHz to 13 MHz	25 µW @ 40 nm	~	~	~	~	~		
	<b>Combo DC/DC</b> Mainstream DC/DC + LQ LDO	Analog microphone	MFCCdata IRQ upon voice detection	32 kHz RC or 32 kHz crystal	7 μW @ 22 nm		0			~		

**Digital audio IP platform** SNR THD+N Main Clock Number of channels 12 or 11 MHz PDM to PCM converter or 19.2 MHz 117 dB -110 dB From 1 to 8 Digital microphone input 12.288 MHz or 11.2896 MHz 12 or 11 MHz or 19.2 MHz Digital audio DAC 110 dB -95 dB From 1 to 8  $\sim$ 12.288 MHz or 11.2896 MHz PWM 24 or 26 MHz or 24.576 MHz Asynchronous Sample Rate Converter  $\checkmark$  $\checkmark$ 114 dB -114 dB From 1 to 8 ASRC

Phase alignment: Ensure 0°phase mismatch between channels for accurate beamforming. PLL-lessU: se standardc locka vailablei n your systema nd save an audio PLL.

DIGITAL AUDIO

Low latency filters: Enable  $\mu s$  latency for applications like ANC or RNC.

Asynchronous interface: Enable to contrai your audio codec as asynchronous slave.

O = roadmap / Under development

	Power Metering Single and Tri-phase IP											
		Power supply	Class	Range	Outpur data rate	Power & computation Engine (PCE)	130nm	40nm				
24-bit DAC	Legacy Serie	2.8 V to 5.5 V	0,1	up to 7000	4 ksps	Yes	$\checkmark$					
	Mainstream Series	2.8 V to 5.5 V	0,1	up to 7000	4, 8, 16, 32 ksps	Yes		~				



## DOLPHIN SEMICONDUCTOR EMPOWERS YOUR CREATIVITY

Dolphin Semiconductor is a leading provider of semiconductor IP solutions, specializing in IP design targeting markets such as Automotive, Industrial, Personal electronics and IoT.

Dolphin's cutting-edge technology IPs in Power management, High-quality Audio, Power metering and Design safety/robustness, allows their thousand customers and partners to accelerates design cycles, fosters faster time-to-market and builds products/ solutions that address the challenges of any industries and support a more sustainable world.



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