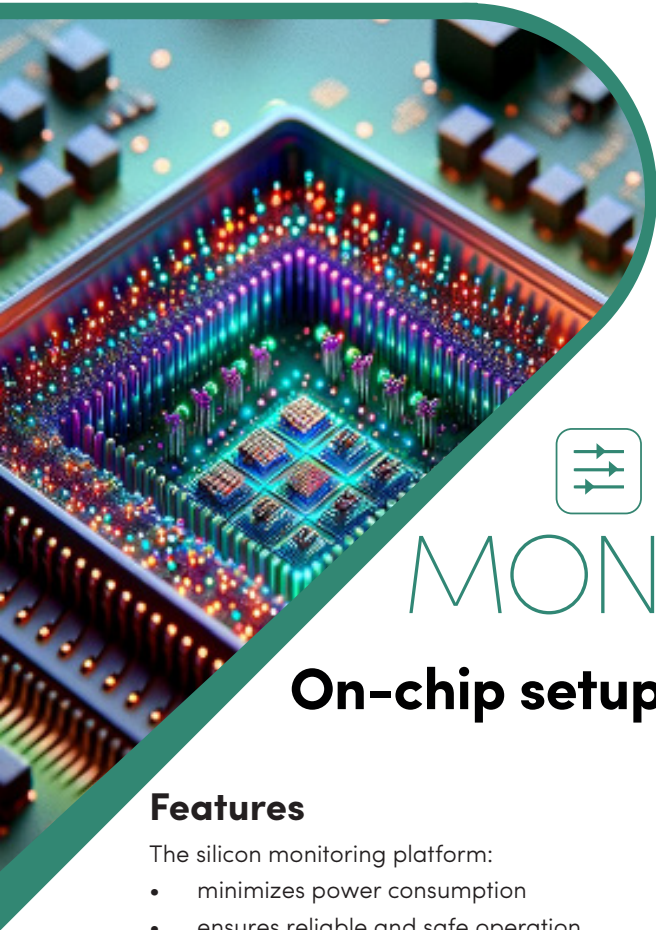




SILICON MONITORING

On-chip setup slack monitors

Features

The silicon monitoring platform:

- minimizes power consumption
- ensures reliable and safe operation
- cuts production test costs
- enables lifecycle management

The platform accurately monitors the setup slack margin of your digital cores in real-time. Dynamically sensing the impact of the operating conditions at data level enables:

- significant power savings through AVS optimization
- prevention of data corruption via ultra-fast triggering of safety mechanisms
- performances maximization with a combined control of the supply voltage and clock frequency
- predictive maintenance thanks to accurate sensing of the aging impacts
- minimization of design margins & specification spread

APPLICATIONS

> HPC / SoCs

> Consumer CPUs

> Industrial MCUs

> Automotive MCUs

> Edge-AI SoCs

